AMENDMENTS TO THE CLAIMS:

Please cancel claims 13, 15, 16, 18, 19, 22, 26, 29 and 30, without prejudice. This listing of claims will replace all prior versions and listings of claims in the Application:

Claims 1-6 (canceled)

Claim 7 (previously presented): A variable drive current driver circuit, comprising:

a pair of push-pull circuits for driving a load circuit complementarily;

a first current source circuit for having a first bias current flow to said pair of push-pull circuits;

a second current source circuit for having said first bias current flow from said pair of push-pull circuits;

a third current source circuit capable of having a second bias current flow to said pair of push-pull circuits;

a fourth current source circuit capable of having said second bias current flow from said pair of push-pull circuits; and

a control circuit for varying both said second bias current driven by said third current source circuit and said second bias current driven by said fourth current source circuit according to a control signal,

wherein each push-pull circuit comprises two transistors having their gates directly connected together.

Claim 8 (previously presented): The variable drive current driver circuit according to claim 7, wherein

said control circuit controls whether said second bias current driven by said third current source flows to said pair of push-pull circuits, and

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said control circuit controls whether said second bias current driven by said fourth current source flows from said push-pull circuits.

Claim 9 (previously presented): A variable drive current driver circuit, comprising:

a pair of push-pull circuits for driving a load circuit complementarily;

a first current source circuit for having a first bias current flow to said pair of push-pull circuits;

a second current source circuit for having said first bias current flow from said pair of push-pull circuits;

a third current source circuit capable of having a second bias current flow to said pair of push-pull circuits;

a fourth current source circuit capable of having said second bias current flow from said pair of push-pull circuits; and

a control circuit for varying both said second bias current driven by said third current source circuit and said second bias current driven by said fourth current source circuit according to a control signal,

wherein each push-pull circuit comprises at least two conductive types of transistors having their gates directly connected together.

Claim 10 (previously presented): The variable drive current driver circuit according to claim 9, wherein

said control circuit controls whether said second bias current driven by said third current source flows to said pair of push-pull circuits, and

said control circuit controls whether said second bias current driven by said fourth current source flows from said push-pull circuits.

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Claim 11 (previously presented): A variable drive current driver circuit, comprising:

a pair of push-pull circuits for driving a load circuit complementarily;

a first current source circuit for having a first bias current flow to said pair of push-pull circuits;

a second current source circuit for having a second bias current flow from said pair of push-pull circuits;

a third current source circuit capable of having a third bias current flow to said pair of push-pull circuits;

a fourth current source circuit capable of having a fourth bias current flow from said pair of push-pull circuits; and

a control circuit for varying both said third bias current driven by said third current source circuit and said fourth bias current driven by said fourth current source circuit according to a control signal,

wherein each push-pull circuit comprises two transistors having their gates directly connected together.

Claim 12 (previously presented): The variable drive current driver circuit according to claim 11, wherein

said control circuit controls whether said third bias current driven by said third current source flows to said pair of push-pull circuits, and

said control circuit controls whether said fourth bias current driven by said fourth current source flows from said push-pull circuits.

Claim 13 (canceled)

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Claim 14 (previously presented): The variable drive current driver circuit according to claim 7,

wherein said first bias current is a constant current.

Claims 15-16 (canceled)

Claim 17 (previously presented): A variable drive current driver circuit comprising:

a pair of push-pull circuits for driving a load circuit complementarily;

a first current source circuit for having a first bias current flow to said pair of push-pull circuits;

a second current source circuit for having said first bias current flow from said pair of push-pull circuits;

a third current source circuit capable of having a second bias current flow to said pair of push-pull circuits;

a fourth current source circuit capable of having said second bias current flow from said pair of push-pull circuits; and

a control circuit for turning on or off both said second bias current driven by said third current source circuit and said second bias current driven by said fourth current source circuit according to a control signal,

wherein each push-pull circuit comprises at least two different types of transistors, and

wherein said first bias current is a constant current.

Claim 18-19 (canceled)

Claim 20 (previously presented): The variable drive current driver circuit according to claim 11,

wherein said first bias current is a constant current.

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Claim 21 (previously presented): The variable drive current driver circuit according to claim 11,

wherein said second bias current is a constant current.

Claim 22-23 (canceled)

Claim 24 (previously presented): A variable drive current driver circuit, comprising:

a pair of push-pull circuits for driving a load circuit complementarily;

a first current source circuit for having a first bias current flow to said pair of push-pull circuits;

a second current source circuit for having a second bias current flow from said pair of push-pull circuits;

a third current source circuit capable of having a third bias current flow to said pair of push-pull circuits;

a fourth current source circuit capable of having a fourth bias current flow from said pair of push-pull circuits; and

a control circuit for varying both said third bias current driven by said third current source circuit and said fourth bias current driven by said fourth current source circuit according to a control signal,

wherein each push-pull circuit comprises at least two conductive types of transistors having their gates directly connected together.

Claim 25 (previously presented): The variable drive current driver circuit according to claim 24, wherein

said control circuit controls whether said third bias current driven by said third current source flows to said pair of push-pull circuits, and

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said control circuit controls whether said fourth bias current driven by said fourth current source flows from said push-pull circuit.

Claim 26 (canceled)

Claim 27 (previously presented): A variable drive current driver circuit, comprising:

a pair of push-pull circuits for driving a load circuit complementarily;

a first current source circuit for having a first bias current flow to said pair of push-pull circuits;

a second current source circuit for having a second bias current flow from said pair of push-pull circuits;

a third current source circuit capable of having a third bias current flow to said pair of push-pull circuits;

a fourth current source circuit capable of having a fourth bias current flow from said pair of push-pull circuits; and

a control circuit for turning on or off both said third bias current driven by said third current source circuit and said fourth bias current driven by said fourth current source circuit according to a control signal,

wherein each push-pull circuit comprises at least two different types of transistors, and

wherein said first bias current is a constant current.

Claim 28 (previously presented): A variable drive current driver circuit, comprising:

a pair of push-pull circuits for driving a load circuit complementarily;

a first current source circuit for having a first bias current flow to said pair of push-pull circuits;

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a second current source circuit for having a second bias current flow from said pair of push-pull circuits;

a third current source circuit capable of having a third bias current flow to said pair of push-pull circuits;

a fourth current source circuit capable of having a fourth bias current flow from said pair of push-pull circuits; and

a control circuit for turning on or off both said third bias current driven by said third current source circuit and said fourth bias current driven by said fourth current source circuit according to a control signal,

wherein each push-pull circuit comprises at least two different types of transistors, and

wherein said second bias current is a constant current.

Claim 29-30 (canceled)

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